4 **(a)** The following logic circuit can be written as P = (NOT A) AND B
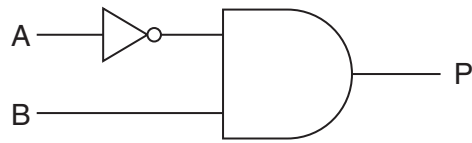

Complete the following truth table for the circuit given above.

A	B	P
0	0	0
0	1	
1	0	
1	1	

[3]

(b) Draw the circuit diagram which will represent the circuit P = NOT (A AND B)

[2]